<u>REMARKS</u>

Claims 23, 25, 28, 32-36 stand rejected under 35 U.S.C. §103(a) as being unpatentable over Lien et al. (U.S. Patent No. 6,493,050) in view of Kadota et al. (U.S. Patent No. 5,818,550). In response, Applicants amended independent claim 23 to clarify that one of the plurality of bus lines is located directly below the columnar spacer, and another of the plurality of bus lines separates a first resin color filter layer of the resin color filter layer from a second resin color filter layer of the resin color filter layer thin film transistor substrate when viewed in a direction perpendicular to the thin film transistor substrate, and respectfully traverse the rejection.

As amended, independent claim 23 now includes the features of claim 37.

Accordingly, withdrawal of this rejection is respectfully requested.

Claims 26-27 stand rejected under 35 U.S.C. §103(a) as being unpatentable over Lien in view of Kadota, and further in view of Kurematsu et al. (U.S. Patent No. 5,764,318). Applicants traverse the rejection for the reasons recited above with respect to the rejection of independent claim 23.

Since amended claim 23 now includes the feature of claim 37, withdrawal of the §103(a) rejection of claims 26-27 is respectfully requested.

Claim 37 stands rejected under 35 U.S.C. §103(a) as being unpatentable over Lien, Kadota, and further in view of Ikeda et al. (U.S. Patent No. 6,671,025). Since this claim is cancelled, the rejection is now moot. However, as this rejection applies to amended claim 23, Applicants respectfully traverse the rejection because none of the cited references disclose or suggest a liquid crystal display that includes one of the

plurality of bus lines located directly below the columnar spacer, and another of the plurality of bus lines separating a first resin color filter layer of the resin color filter layer from a second resin color filter layer of the resin color filter layer thin film transistor substrate when viewed in a direction perpendicular to the thin film transistor substrate.

As shown in FIG. 24 of the present Application, a columnar spacer 30 has a drain bus line 26 located directly below the columnar spacer. Another drain bus line adjacent to the drain bus line under the columnar spacer separates a first resin color filter layer R from a second color filter resin layer G of the resin color filter layer thin film transistor substrate when viewed in a direction perpendicular to the thin film transistor substrate. Applicants respectfully submit that none of the cited references discloses or suggests this feature.

As acknowledged by the Examiner, Ikeda merely discloses a spacer as a combination of elements 113/117/115 that are directly formed above the drain bus line 103a and gate bus line 103b. However, Ikeda fails to disclose or suggest the bus lines being located directly below the columnar spacer and another one in a location that separates the first and second resin color filter layers of the resin color filter layer thin film transistor substrate when viewed in a direction perpendicular to the thin film transistor substrate. Since neither Kadota nor Lien overcome the deficiencies of Ikeda, Applicants respectfully submit that the combination of these references fails to disclose or suggest the above-described feature. For this reason, withdrawal of the §103(a) rejection as it applies to amended claim 23 is respectfully requested.

For all of the foregoing reasons, Applicants submit that this Application is in condition for allowance, which is respectfully requested. The Examiner is invited to contact the undersigned attorney if an interview would expedite prosecution.

If a Petition under 37 C.F.R. §1.136(a) for an extension of time for response is required to make the attached response timely, it is hereby petitioned under 37 C.F.R. §1.136(a) for an extension of time for response in the above-identified application for the period required to make the attached response timely. The Commissioner is hereby authorized to charge any additional fees which may be required to this Application under 37 C.F.R. §§1.16-1.17, or credit any overpayment, to Deposit Account No. 07-2069.

Respectfully submitted,

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